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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Sebastien KERDILES et al.

Confirmation No. 8821

Application No.: 10/733,470

Group Art Unit: 3671

Filing Date: December 12, 2003

Examiner: L.K. Tran

For: TOOLS AND METHODS FOR  
DISUNITING SEMICONDUCTOR WAFERS

Atty. Docket No.: 4717-9100

**RESPONSE TO RESTRICTION REQUIREMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

In response to the Examiner's restriction requirement dated August 9, 2005, applicants elect, with traverse, the precision device claims of Group I, claims 1-23, for prosecution in this application.

It is believed that the entire application is in condition for allowance, early notice of which would be appreciated.

No fees are believed to be due for this submission. Please charge any required fees to Winston & Strawn LLP Deposit Account No. 50-1814.

Respectfully submitted,

Date: 8/17/2005

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